

DEVICE AND METHOD FOR CALIBRATING LINEAR VELOCITY AND
TRACK PITCH FOR OPTICAL DISC DRIVE

BACKGROUND OF THE PRESENT INVENTION

A. Field of the present invention

5 The present invention relates to a device and method for calibrating a linear velocity and a track pitch for an optical disc drive.

B. Description of the Related Art

Optical recording media, such as CD-Rs, CD-RWs, CD-ROMs, DVDs, DVD-RAMs, are usually read by optical disc drives. These optical disc drives
10 correctly control the rotation speed of the spindle motor according to a reproduced sync signal or a motor frequency generator pulse signal. In addition, a precise calculation has to be performed using a “time-to-track” transformation mechanism, so that the laser beam can be shifted to a correct position of the disc (i.e., track jumping). The mechanism of “time-to-track” has to use the parameters of the linear
15 velocity and the track pitch as a calculation basis. According to the current CD standard, the recording linear velocity must be between 1.2 and 1.4 m/s, while the track pitch must be between 1.5 and 1.7 μ m. However, the writing speed and track pitch during actual disc playing cannot be predicted. As a result, a slowest writing speed and a maximum track pitch are used as the initial calculation basis in the prior
20 art. Then, the values of the linear velocity and the track pitch are sequentially modified according to the relationship between the track number, which is actually fed back during the track jumping, and the time. However, the laser beam cannot be correctly and quickly shifted to the correct track in this method, thereby wasting a long period of seeking time.

To solve this problem, a method for calibrating an optical disc drive, which controls the spindle motor with a constant linear velocity (hereinafter referred to as CLV), has been disclosed. This method, however, cannot calibrate an optical disc drive, which controls the spindle motor with a constant angular velocity (hereinafter referred to as CAV). The prior art method calculates the linear velocity according to the ratio of the frame-synchronous signal and disk rotation period. In addition, the prior art method can only calibrate the linear velocity with respect to optical discs having data recorded thereon, but not the blank discs.

The linear velocity is an important index for measuring the data amount and is helpful to positioning the laser beam precisely. It is therefore an urgent to-be-solved problem as to how to effectively calibrate the linear velocity and how to apply the calibration method to both the CLV mode and the CAV mode, and to the blank discs.

SUMMARY OF THE PRESENT INVENTION

It is therefore an object of the present invention to provide a method for calibrating a linear velocity and a track pitch for an optical disc drive. The method can provide a high-precision calibration regardless of the type of the disc, which may be a blank disc or a recorded disc.

Another object of the present invention is to provide a calibration method applicable to any control mode of the spindle motor. The calibration method can provide a high-precision calibration on both the CLV and CAV control modes.

To achieve the above-mentioned objects, the present invention provides a calibration device for calibrating linear velocity and track pitch for an optical disc drive. The device includes a bit-clock generator, a counter, and a calculator. The bit-clock generator generates a bit-clock signal having a frequency higher than that

of a reproduced signal according to the reproduced signal read from an optical recording medium. The counter counts the clocks of the bit-clock generating unit to generate a data amount during a motor frequency generator pulse. The calculator calculates the linear velocity and the track pitch of the optical recording medium 5 according to the data amount and the motor frequency generator pulse.

The present invention further provides a method for calibration linear velocity and track pitch for an optical disc drive. The method includes the following steps. First, the optical disc drive is initialized. Then, the type of the optical recording medium is determined to be a blank disc or a recorded disc. Next, the laser beam is 10 moved to the lead-in area. Then, compare the frequency generator pulse of the motor with the reproduced signal of the optical recording medium, so as to calculate a first data amount. Next, calculate the linear velocity of the optical recording medium according to the first data amount and a calibration equation. Then, move the pick-up to jump any K tracks and calculate the number of data blocks passed. 15 Calculate a second data amount. Next, calculate the track pitch according to the linear velocity of the optical recording medium, the first and second data amount, and a track-jumping equation.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and advantages of the present invention will become 20 apparent by reference to the following description and accompanying drawings wherein:

FIG. 1 is a functional block diagram showing the linear velocity calibration circuit of the present invention;

FIG. 2 is a synchronous bit-clock generator of the present invention under a 25 CAV mode;

FIG. 3 is a synchronous bit-clock generator of the present invention under a CLV mode; and

FIGS. 4A to 4B are flow charts showing the calibration steps of the present invention.

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DETAIL DESCRIPTION OF THE PRESENT INVENTION

Two signals are used to compute the linear velocity in the present invention. The first signal is a frequency generator pulse (hereinafter referred to as a FG pulse), which is generated when the spindle motor in the optical disc drive rotates. The second signal is a playback signal or a reproduced signal, (e.g., an EFM (eight-bit to fourteen-bit modulation) signal or a wobble signal) read from an optical recording medium.

10 FIG. 1 is a block diagram showing a device for calibrating the linear velocity and track pitch of the present invention. As shown in the FIG. 1, a calibration device 10 includes a frequency divider 11, a synchronous bit-clock generator 12, a counter 13, a linear velocity and track pitch calculator 14.

15 The optical disc drive utilizes a motor speed controller (not shown) to control the rotation speed of the optical recording medium. The motor speed controller generates the FG pulses, for example 6 pulses per rotation of the spindle motor. The frequency of the FG pulses is defined as "Y". The system can calculate the number of revolutions or the values of the rotation angles by using the FG pulses, and control the motor speed in constant angular velocity. The frequency divider 11 generates a rotation period signal (FG/X) corresponding to the rotation speed of the optical recording medium according to the FG pulses and a setting value (X). If both the Y and X are 6, it means that the calculator of the calibration device 10 is one revolution of the spindle motor. That is, after the optical recording medium rotates

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a revolution, the frequency divider 11 generates a pulse as the rotation period signal.

Reproduced signals can be read from the optical recording medium with data recorded thereon. For example, the EFM signals may be read from the recording medium, which has been recorded data. The ATIP or wobble signals can be read from the recording medium without data recorded thereon. The synchronous bit-clock generator 12 generates a high-frequency bit-clock BC according to the reproduced signal. The synchronous bit-clock generator 12 is a phase-locked loop (PLL) circuit, and generates a bit-clock with an N-time frequency according to the input signal. At the same time, when the synchronous bit-clock generator 12 reaches a locked state, it will output a PLL-locked indication signal. The synchronous bit-clock generator 12 has different architectures under the CLV and CAV control states, and the details of the architectures will be described in the following.

After the synchronous bit-clock generator 12 outputs a PLL-locked indication signal, the counter 13 counts the pulse number of the bit-clock BC according to the rotation period signal, and generates a data amount. The counter 13 is for counting the pulse number of the bit-clock BC. Thereby, how many the data amount of the optical recording medium is recorded between two rotation period signals can be determined. The counter 13 may also output the data amount to the calculator 14.

The calculator 14 is used to calculate the linear velocity and the track pitch. The calculator 14 calculates the linear velocity β according to Equation (1):

$$\frac{X}{Y} \times \frac{2 \cdot \pi \cdot R}{\beta} \times C = M \quad (1)$$

wherein X/Y represents the number of revolutions of the optical recording medium, M represents the data amount outputted from the counter 13, i.e., the data amount per revolution of the rotation period signal (FG/X), C represents the output

data amount of the optical recording medium per unit time, and R represents the position or the radius of the pick-up located on the optical recording medium. According to the specification of the optical recording medium, the radius R from the center of the physical disc to the location of the lead-in 0:2:0 (0th minute, 2nd second and 0th block) is 25mm. Therefore, R can be regarded as a known parameter when calculating M at the location of 0:2:0. Since X, Y, R, C, and M are known, the linear velocity β having the unit of m/s can be calculated.

When Equation (1) is used to calculate the linear velocity, different reproduced signals will be provided different values of C. For instance, if the reproduced signal is the EFM signals and the calculation pulse is the bit-clock, C is 75*98*588, and M represents the bit amount contained in each FG/X pulse. According a general specification, 75 data blocks can be recorded on the optical recording medium, each data block contains 98 EFM frames, each of EFM frame contains 588T (data bit clocks). As can be known form the above-mentioned information, there are 75*98*588 bit-clocks contained in a period of time when the optical recording medium rotates for one second.

Taking the CD-R or CD-RW for an example, if the calculation pulse is a wobble signal, C is 22050, and M represents the wobble-clock amount contained in each period of the FG/X pulse. If the calculation pulse is a biphase clock, C is 6300, and M represents the biphase clock amount contained in each period of the FG/X pulses.

Because X, Y, R, C, and M are all known parameters, the linear velocity β can be surely obtained. At the same time, because the frequency of the bit-clock BC is high, the precision of the linear velocity β is correspondingly high. As for the wobble signal or the bi-phase clock, since the resolution thereof is poor, the

frequency of the signal, wobble signal or the bi-phase clock, can be multiplied to reach the resolution of the bit-clock BC. For example, the frequency of the wobble signal can be multiplied by 196, and the frequency of the bi-phase clock can be multiplied by 686. As a result, the obtained precision of the linear velocity β can 5 be the same as that of the bit-clock BC.

After the value of the linear velocity β is obtained, the track pitch t can be further calculated. The present invention derives Equation (2) to calculate the track pitch t :

$$\frac{n}{75} \times \beta = 2 \cdot \pi \cdot \left(\frac{R_1 + R_2}{2} \right) \times \left(\frac{R_1 - R_2}{t} \right) \quad (2)$$

10 wherein n represents the total data blocks that have been jumped, and 75 represents the data blocks passed within one second. The $n/75$ represents the elapsed time from the location of 0:2:0 (0th minute, 2nd second and 0th block) to another location jumped by n data blocks. The $n/75 \times \beta$ represents the spiral distance there between.

15 In Equation (2), $R_1=25\text{mm}$ is known, and the data amount M_1 and M_2 can be obtained from the calculator 14, but the value of R_2 is unknown. Fortunately, the linear proportion relationship between M and R can be derived from Equation (1). Therefore,

$$\frac{R_1}{R_2} = \frac{M_1}{M_2} ,$$

$$20 \Rightarrow R_2 = \frac{M_2}{M_1} \times R_1 \quad (3).$$

The value of the track pitch t can be obtained from Equations (2) and (3) as follows:

$$t = \frac{75\pi}{n\beta} \cdot \left(\left(\frac{M_2}{M_1} \right)^2 - 1 \right) \cdot R_1^2 \quad (4).$$

As can be understood from the description mentioned above, it is possible for the architecture of FIG. 1 to use a high-precision bit-clock as a basis for calculating the linear velocity, so the resolution of the calculated track pitch is better. The reading of the reproduced signal of the present invention is not limited to the usage 5 of the EFM sync signal, the wobble signal also can be used to instead. As a result, the calibration result with high precision may be obtained even for a blank optical recording medium. Moreover, the linear velocity calibration of the present invention is also applicable to both the CLV mode and the CAV mode.

FIG. 2 shows an embodiment of the synchronous bit-clock generator that is 10 applied to the CAV mode. The rotation speed of the spindle motor is constant in the CAV mode. However, the tangential velocities at different radii of the optical recording medium are different. That is, the feature pattern frequency of the reproduced signal changes with the change of the radius. The feature extractor 21 reads the reproduced signals (e.g., EFM or ATIP signals) from the optical recording 15 medium, determines the type of the optical recording medium, and extracts the feature pattern thereof, such as sync signals, as the reference signals inputted to the phase-locked loop (PLL) circuit 22. For example, the feature pattern of the reproduced signal of the optical recording medium with data recorded thereon is the EFM sync signal, while the feature pattern of the reproduced signal of the blank 20 optical recording medium is the ATIP sync signal, bi-phase clock or wobble clock.

The PLL circuit 22 includes an N divider 221, a phase frequency detector 222, a loop filter and voltage controlled oscillator 223. Since the function and the architecture of the PLL circuit 22 are the same as those in a general PLL circuit, the detailed descriptions are omitted. The bit-clock generator 12 traces the frequency 25 change of the feature pattern owing to the radius change of the location of the

pick-up to generate the bit-clock signal by using the PLL circuit 22. In addition, since the PLL circuit 22 includes an N divider 221, where N can be set as 588 when taking the EFM as an example, so as to generate a bit-clock signal having the same frequency as that of the data bit of the recording medium.

5 FIG. 3 shows an embodiment of the synchronous bit-clock generator applied to the CLV mode. The rotation speed of the spindle motor is not constant in the CLV mode. However, the tangential velocities at different radii of the optical recording medium are the same. The difference between FIGS. 3 and 2 resides in that the embodiment of FIG. 3 sets the reference signal (bit-clock) to be one with a constant
10 frequency, obtains the desired linear velocity by controlling the rotation speed of the motor, and generates a reproduced feature pattern capable of locking the reference signal. In the embodiment of FIG. 2, however, the rotation speed of the motor is regarded as a constant value.

As shown in FIG. 3, a frequency divider 311 divides the frequency of the
15 constant bit-clock by N. A phase frequency detector 315 and a loop filter 316 receive the output signals from the frequency divider 311 and a feature extractor 314, and generate the control signals for a control unit 312. The control unit 312 controls the rotation speed of the spindle motor of the disc drive according to the control signals, so as to keep the same linear velocity for the disc placed thereon.
20 The RF signal generator 313 is used to read the reproduced signal of the disc. The feature extractor 314 extracts the feature pattern of the reproduced signal as the feedback signal for the PLL circuit, and determines the type of the reproduced signal on the optical recording medium. The function and method are the same as those in the embodiment of FIG. 2.

25 Compared to the functional block diagram of FIG. 1, if the embodiment of FIG.

2 is adopted by the synchronous bit-clock generator 12, it means that the frequency of the FG pulse is a constant value. On the contrary, if the embodiment of FIG. 3 is adopted, it means that the frequency of the FG pulse changes under the control of the motor speed controller 312 in the synchronous bit-clock generator 12. In fact, FIGS. 5 2 and 3 have the similar design rules in which the FG pulse of the motor and the reproduced signal are adopted.

According to the descriptions mentioned above, the flow chart of the method of calibrating the linear velocity and track pitch of the present invention is shown in FIG. 4. From the calculations mentioned above, the calibrated linear velocity β 10 and track pitch t can be obtained.

Steps 401 to 404 are generalized initial processes. First, at step 401, the optical pick-up (OPU) is moved to the lead-in area. Then, at step 402, the laser beam is activated to focus so as to read the RF signal that is reflected back from the optical recording medium. Next, at step 403, the motor is controlled to be the CLV 15 mode or the CAV mode. Then, at step 404, the operations of track-locking and reading are performed so as to align the track and read the RF reproduced signal.

At step 405, it is judged that whether the optical recording medium contains data according to whether an EFM signal is contained in the RF reproduced signal. If the optical recording medium is a blank medium, the ATIP signal is used as a basis 20 for calibrating the signal. Otherwise, the EFM signal is used as a basis for calibrating the signal. Then, at step 406, the optical recording medium is configured to locate at 0:2:0, the initial position of the data record. That is, the optical recording medium is configured to locate at a position of radius of 25 mm (R_1) of the optical recording medium.

25 Then, the calculator in FIG. 1 is activated to compare the frequency generator

5 pulse of the motor with the reproduced signal of the optical recording medium, so as to obtain the value of the first data amount M_1 . Next, at the step 407, the linear velocity β of the optical recording medium are calculated according to the first data amount M_1 and the above-mentioned equations for calculating the linear velocity.

10 Then, at step 408, the OPU is moved by any K tracks to the location of a radius of R_2 to measure the time code for reaching the location and to calculate the data blocks n or block number passed by the jumping of K tracks. Next, at step 409, the calculator is again activated to compare the frequency generator pulse of the motor 15 with the reproduced signal of the optical recording medium, so as to calculate the value of the second data amount M_2 .

15 After the values of M_1 , M_2 , and the linear velocity β is obtained, the value of track pitch t can be obtained from the above-mentioned track-jumping equation (4), so as to precisely get the track pitch. Finally, at step 411, the calibration processes are ended.

20 To sum up, the present invention is applicable to high-precision calibrations under any constant rotation speed regardless of the configuration of the optical disc drive is a CLV mode or a CAV mode. Furthermore, since the present invention uses the bit-clock signal as a measurement unit, the calibration result can be more precise in comparison with the prior art using the EFM sync signal. Moreover, the present invention can use the ATIP signal as a basis for calibration, the linear velocity calibration also can be performed even though no data is recorded on the optical recording medium.

25 While certain exemplary embodiments have been described and shown in the accompanying drawings, it is to be understood that such embodiments are merely

illustrative of and not restrictive on the broad invention, and that this invention not be limited to the specific constructions and arrangements shown and described, since various other modifications may occur to those ordinarily skilled in the art.